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FIG. 11 (PRIOR ART)

DIFFERENCE	SD	MMC	INFLUENCE
INITIALIZATION COMMAND	CMD55+CMD41	CMD1	
CSD VALUE (bit [46:37])	GROUP ERASE PARAMETER DEFINITION	ERASE SECTOR/GROUP PARAMETER DEFINITION	HOST ERASE DEFINITION DIFFERS
COMMAND SUPPORT	ACMDxx FOR SD (CMD55+CMDxx)	SCMDyy FOR S-MMC (CMD55+CMDyy)	COMMAND DIFFERS
OCR VALUE (bit [7:0])	DEFINE VCC = 1.6 TO 2.0V AT bit [7:4]	DEFINE VCC = 1.65 TO 1.95V AT bit [7]	SUPPORT VOLTAGE DEFINITION DIFFERS
OTHER REGISTER	SCR REGISTER (512B) PROVIDED	EX-CSD REGISTER (512B) PROVIDED	SUPPORT FUNCTION AND STATUS CHECK METHOD DIFFER



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FIG. 12
(BACKGROUND ART)

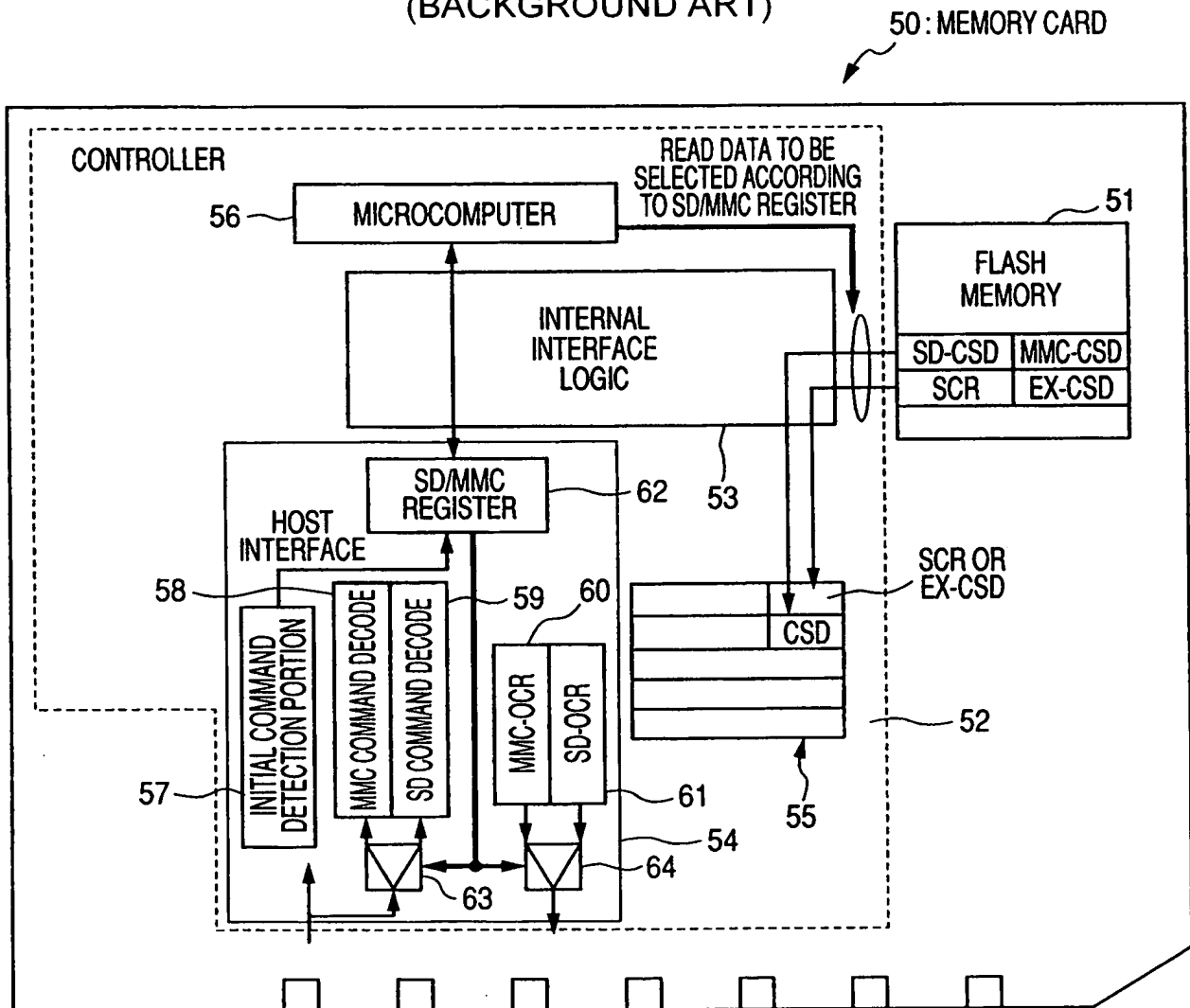
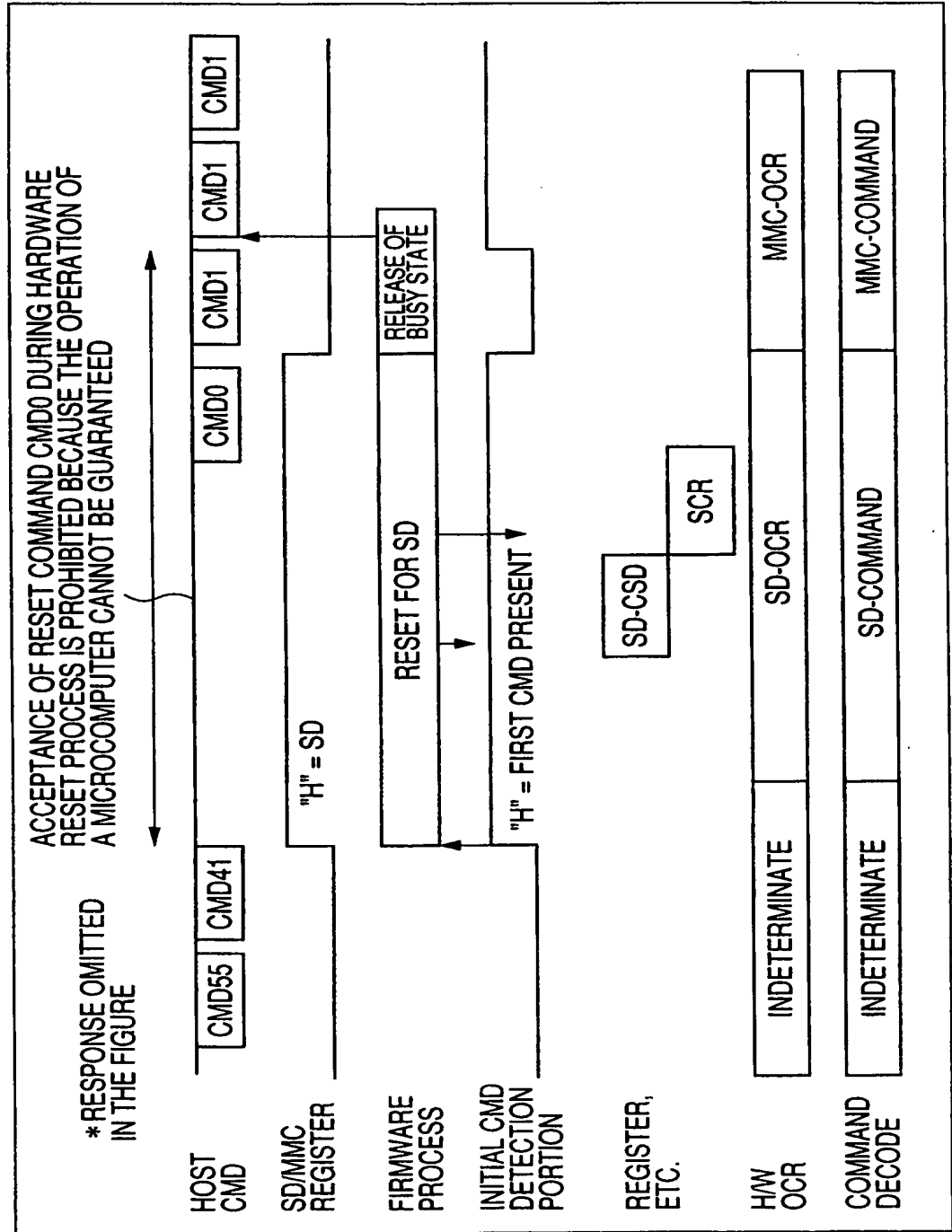


FIG. 13 (BACKGROUND ART)





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FIG. 14
(BACKGROUND ART)

